IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Anticipate	ed Classification
•	his Application:
Class _	Subclass_
P	rior Application
Examiner: Day	rid L. Robertson
	Art Unit: 2186

DIVISIONAL APPLICATION TRANSMITTAL

Mail Stop Patent Application P.O. Box 1450 Alexandria, VA 22313-1450



Dear Sir:

This is a request for filing a divisional application pursuant to 37 C.F.R. § 1.53(b), of pending prior application:

Serial No. 09/130,818 filed August 7, 1998, by Yoshiyuki Tanaka, Makoto Yatabe, Takeaki Sato, and Kazuya Kawamoto (inventors currently of record in prior application) for METHOD FOR CONTROLLING NON-VOLATILE SEMICONDUCTOR MEMORY SYSTEM.

- 1. Enclosed is a replacement specification, which does not add new matter and a copy of the declaration as originally filed.
- 2. \square The filing fee is calculated below:

		C	ALCULAT	ION OF FEES				
ITEM		TOTAL NO. OF CLAIMS		NO. OF CLAIMS OVER BASE	LG/SM \$ ENTITY FEE		\$ AMOUNT	\$ FEE
Α	TOTAL CLAIMS FEE	13	-20	. 0	LG=\$18 SM=\$9	\$18	\$ 0	
В	INDEPENDENT CLAIMS FEE*	9	-3	6	LG=\$86 SM=\$43	\$86	\$516	
С	SUBTOTAL - ADDITIONAL CLAIMS FEE (ADD FINAL COLUMN IN LINES A + B)						\$516	
D	MULTIPLE-DEPENDENT CLAIMS FEE LARGE ENTITY FEE = \$290 SMALL ENTITY FEE = \$145					\$		
E	BASIC FEE LARGE ENTITY FEE = \$770 SMALL ENTITY FEE = \$385						\$710	
F	TOTAL FILING FEE (ADD TOTALS FOR LINES C, D, AND E)						\$1226	
	*LIST INDEPENDENT CLAIMS 1, 2, 4-6, 10, 13, 15, 16							•

- 3. The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Account No. 50-1314. A copy of this sheet is enclosed.
- 4. \boxtimes A check in the amount of \$1,226 is enclosed.

Э.	I	before calculating the filing fee. (At least one original independent claim must be retained for filing purposes.)					
6.		Amend the specification by inserting before the first line the sentence:					
7a.	\boxtimes	New formal drawings are enclosed.					
7b.	\boxtimes	Priority of application Serial No(s).:					
		<u>Japanese</u> application Serial No. <u>214561/1997</u> filed <u>August 8, 1997</u> and <u>Japanese</u> application Serial No. <u>119099/1998</u> filed <u>April 28, 1998</u> is claimed under 35 U.S.C. § 119.					
7c.	\boxtimes	The certified copies of the priority applications were filed in prior application Serial No. <u>09/130,818</u> , filed <u>August 7, 1998</u> .					
8.	\boxtimes	The prior application is assigned of record to: <u>Kabushiki Kaisha Toshiba</u> .					
9.	\boxtimes	The power of attorney in the prior application is as listed on the attached copy of the declaration filed in the patent application.					
		a. The power appears in the original papers in the prior application.					
		Since the power does not appear in the original papers, a copy of the power in the prior application is enclosed.					
		c. Address all future communications to:					
		William H. Wright CORRESPONDENCE ADDRESS:					
		☐ Customer Number 000026021					
		d. An Associate Power of Attorney is enclosed.					
10.	\boxtimes	Change of Correspondence Address is enclosed.					
11.		A preliminary amendment is enclosed. (Claims added by this amendment have been properly numbered consecutively beginning with the number next following the highest numbered original claim in the prior application.)					
12.		Applicant claims small entity status. See 37 CFR 1.27.					
		Applicant no longer claims small entity status.					

13. Transmittal of Information Disclosure Statement and Form PTO-1449 a copies of 17 references.

Date: February 25, 2004

William H. Wright

Registration No. 36,312

HOGAN & HARTSON L.L.P. 500 South Grand Avenue, Suite 1900 Los Angeles, California 90071

Phone: 213-337-6700 Fax: 213-337-6701

Attorney Docket No: 81788.0265

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re ap	oplication of:	Art Unit:	Not Assigned
Yoshiyu	uki Tanaka, et al.	Examiner:	Not Assigned
Serial N	No: Not Assigned		
Filed:	February 25, 2004		·
For:	METHOD FOR CONTROLLING NON-VOLATILE SEMICONDUCTOR MEMORY SYSTEM		
	CERTIFICATE OF MAILING VIA U.S. "Express Mail" Mailing Label No. EL Date of Deposit: February 25 op Patent Application	715813209US	=
P.O. Bo	ssioner for Patents ox 1450 dria, VA 22313-1450		
Dear Si	ir:		
	I hereby certify that ☐ Two copies of Divisional Application Transmittal ☐ check in amount of \$1,226 as filing fee ☐ patent application (78 page(s) of specification; ☐ Transmittal of Formal Drawings and 45 sheet(s) of ☐ executed Declaration and Power of Attorney in par ☐ Change of Corresopndence Address ☐ Information Disclosure Statement, Form PTO-1448 ☐ return postcard	formal drawings ent application S	Serial No. 09/130,818
are bei service	ing deposited with the United States Postal Service "with sufficient postage under 37 C.F.R. § 1.10 on the da	Express Mail Page indicated abor	ost Office to Addressee ve and are addressed to:
	Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Arlington, VA 22313-1450		
Date: F	Eebruary 25, 2004	Roeshene	
500 Soi Los Ang	& Hartson, LLP uth Grand Avenue, Suite 1900 geles, California 90071 one: 213-337-6700	Name of person M Cawbo Signa	mille

Facsimile: 213-337-6701

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Art Unit:

Not Assigned

Yoshiyuki Tanaka, et al.

Examiner: Not Assigned

Serial No: Not Assigned

Filed: February 25, 2004

For:

METHOD FOR CONTROLLING NON-VOLATILE

SEMICONDUCTOR MEMORY SYSTEM

CHANGE OF CORRESPONDENCE ADDRESS

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Please change the correspondence address of the Applicants to that for customer number 000026021, which is presently:

> Hogan & Hartson L.L.P. Biltmore Tower 500 South Grand Avenue, Suite 1900 Los Angeles, California 90071 Telephone: 213-337-6700

Facsimile: 213-337-6701

It is certified that the person whose signature appears below has the authority to change the correspondence address for the above-identified application.

Respectfully submitted,

HOGAN & HARTSON LI

Date: February 25, 2004

William H. Wright

Registration No. 36,312 Attorney for Applicant(s)

500 South Grand Avenue, Suite 1900

Los Angeles, California 90071 Phone: 213-337-6700

Fax: 213-337-6701

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Art Unit: Not Assigned

Yoshiyuki Tanaka, et al.

Examiner: Not Assigned

Serial No: Not Assigned

Filed: February 25, 2004

For: METHOD FOR CONTROLLING NON-

VOLATILE SEMICONDUCTOR MEMORY

SYSTEM

TRANSMITTAL OF FORMAL DRAWINGS

Attn: OFFICIAL DRAFTSPERSON

Mail Stop Patent Application Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Enclosed are the formal drawings (45 sheets - Figures 1-52) in the above-captioned case.

Respectfully submitted,

HOGAN & HARTSON L.L.T

Date: February 25, 2004

William H. Wright

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Los Angeles, California 90071

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